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Appl. No.: 09/679,780
Filed: 10/05/2000

In the United States Patent and Trademark Office

In re the application of:
Fleming Andersen

Filed: 10/05/2000

For: Fully Exhibiting Asynchronous Behavior in a Logic Network Simulation

Appl. No.: 09/679,780

Applicant's Docket:
AUS920000494US1

Group Art Unit: 2121

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Technology Center 2100

Attention: Official Draftsperson
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IN THE DRAWINGS

Formal Drawings are herein submitted as requested in the Office Action.

Respectfully submitted,

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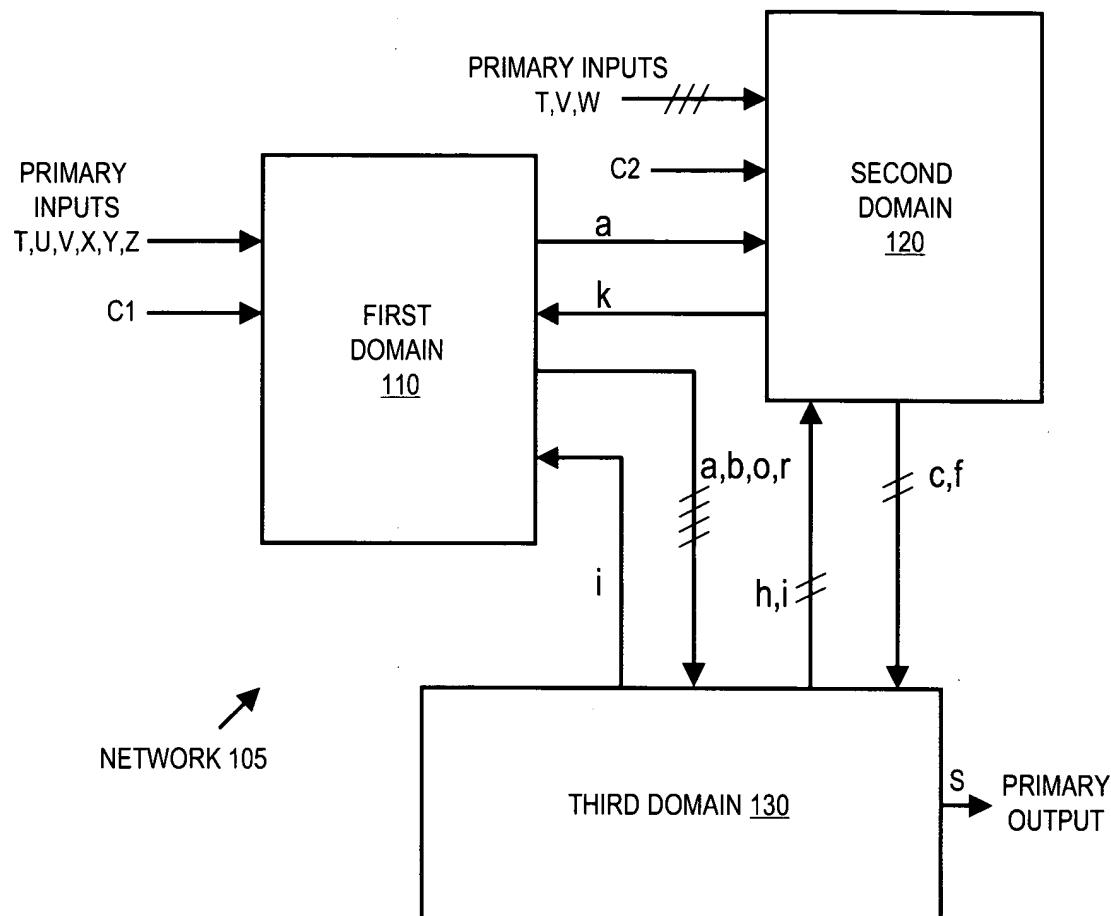


FIG. 1



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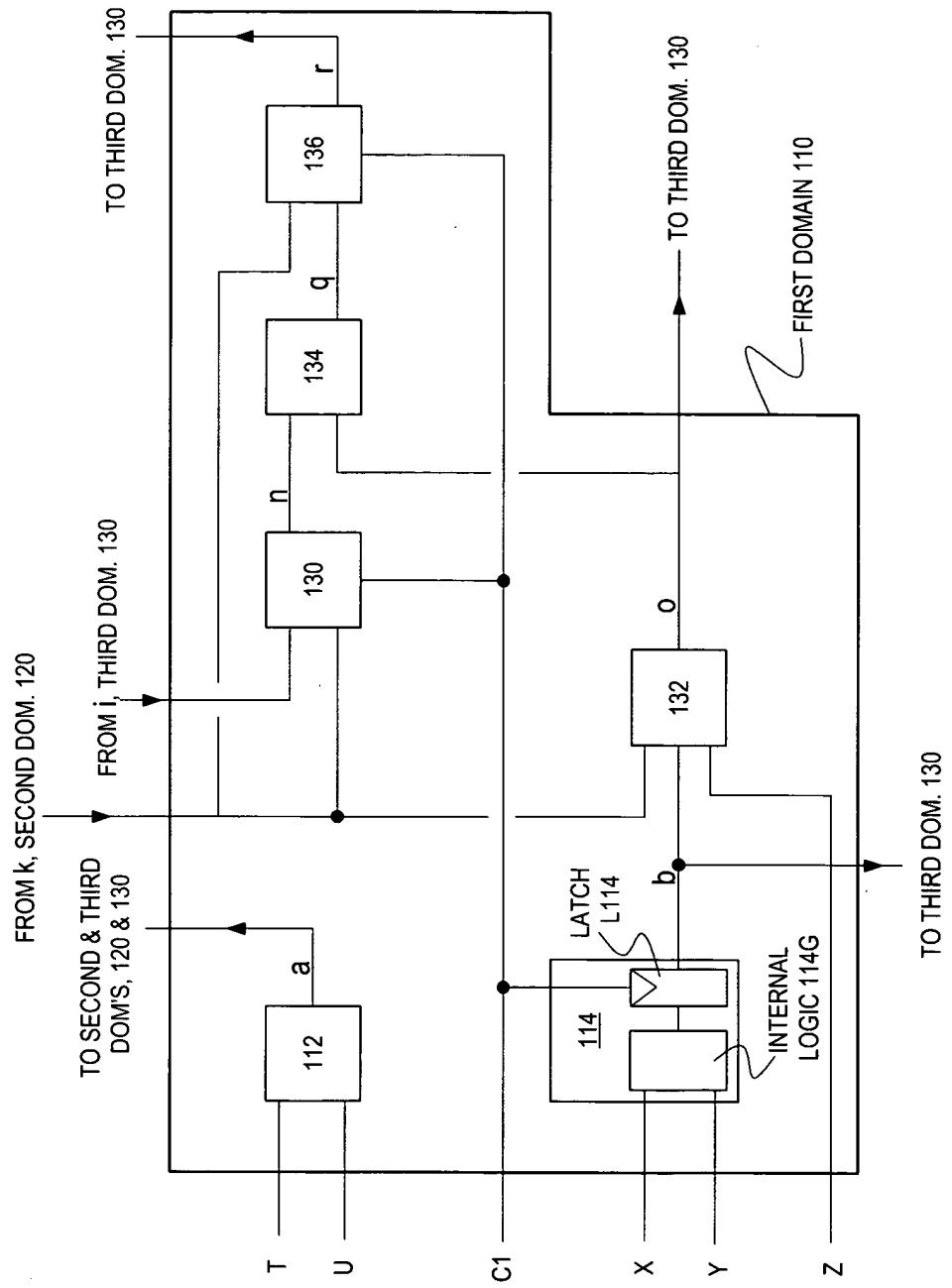


FIG. 2



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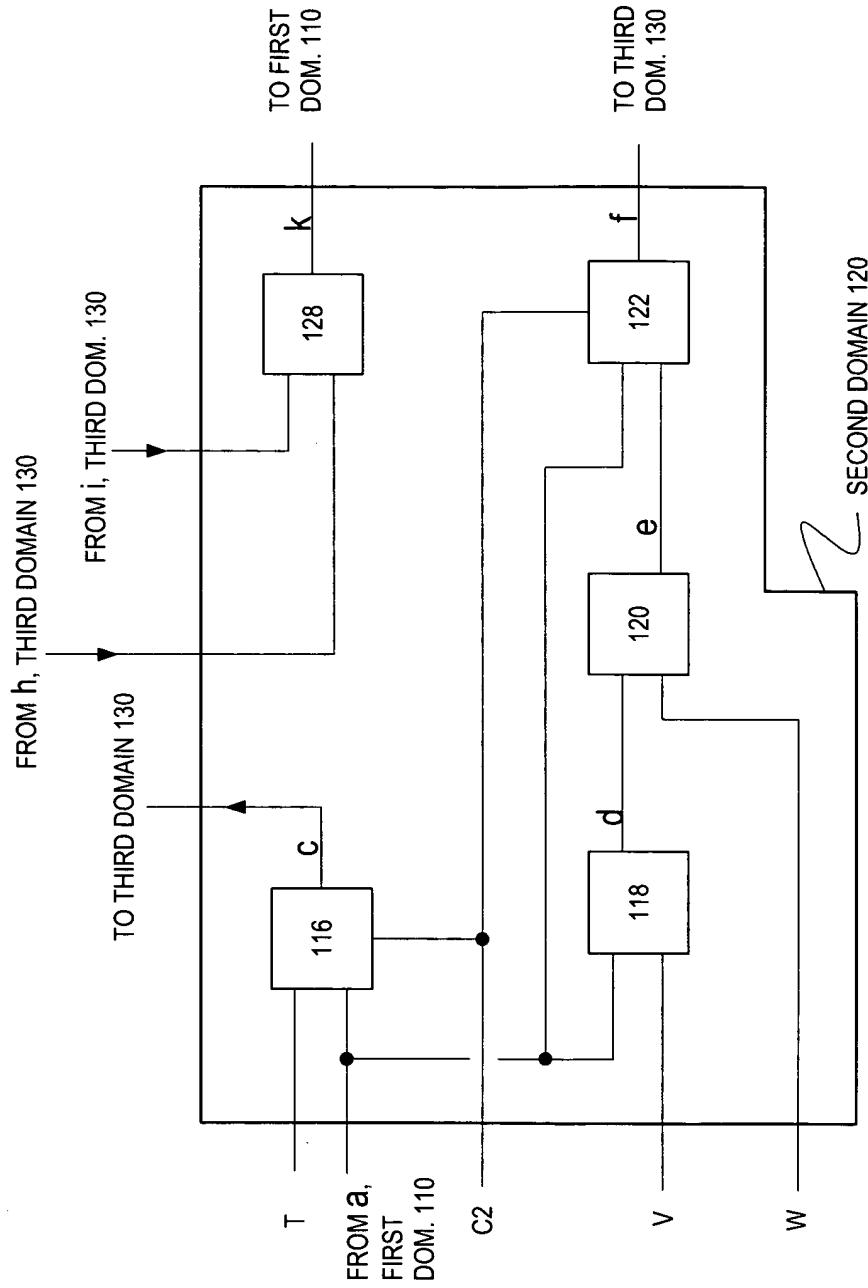


FIG 3



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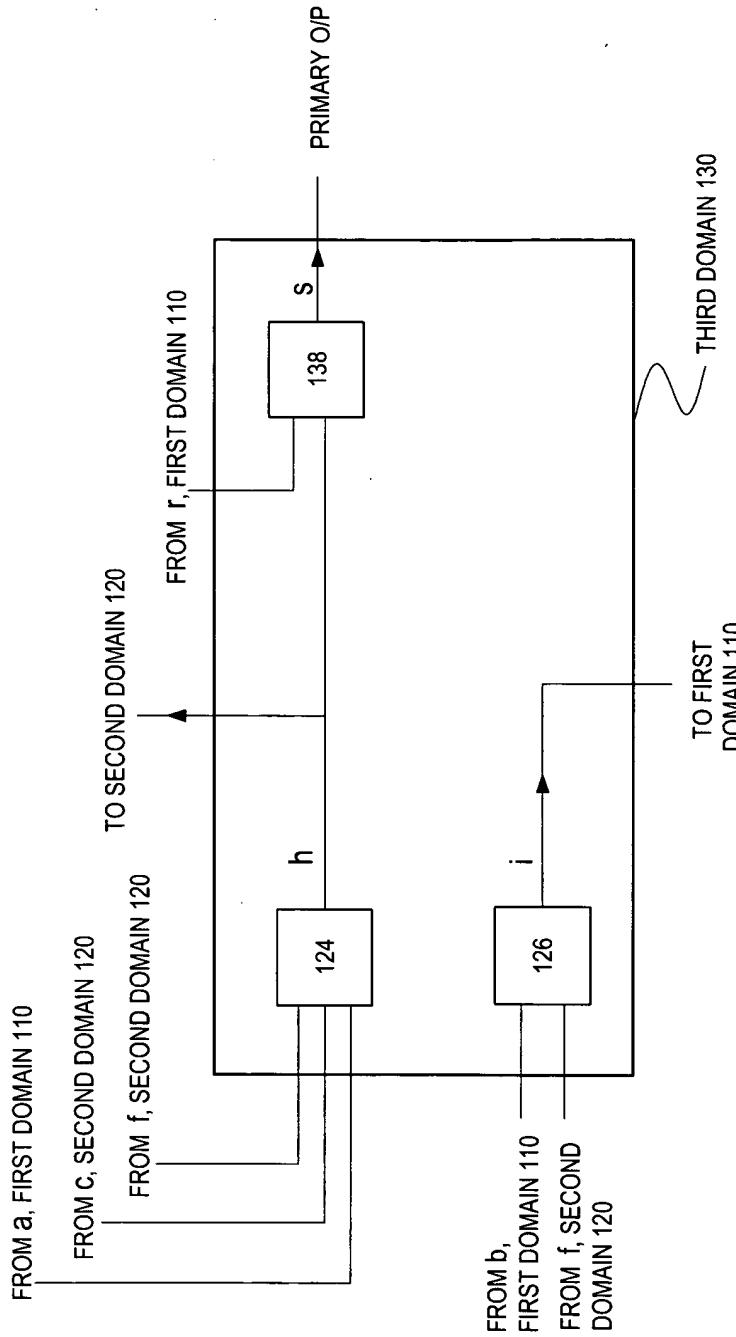


FIG 4



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FIRST DOMAIN OPERATION STACK 510		SECOND DOMAIN OPERATION STACK 520		THIRD DOMAIN OPERATION STACK 530	
1	$r = L136$	1	$c = L116$	1	$h = fn(c, a, f)$
2	$n = L130$	2	$f = L122$	2	$i = fn(f, b)$
3	$b = L114$	3	$L116 = fn(T, a)$	3	$s = fn(h, r)$
4	$a = fn(T, U)$	4	$d = fn(@, Y)$		
5	$L114 = fn(X, Y)$	5	$e = fn(d, W)$		
6	$o = fn(K, b, z)$	6	$L122 = fn(@, e)$		
7	$L130 = fn(i, K)$	7	$k = fn(i, h)$		
8	$q = fn(n, o)$				
9	$L136 = fn(K, q)$				

FIG. 5



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FIRST DOMAIN
OPERATION STACK
610

1	r
2	n
3	b
4	a
5	L114
6	o
7	--
8	--
10	--
11	--
12	--
13	--
14	L130
15	q
16	--
17	--
18	--
19	--
20	--
21	--
22	L136

SECOND DOMAIN
OPERATION STACK
620

1	c
2	f
3	L116
4	--
5	--
6	--
7	--
8	--
9	--
10	--
11	--
12	--
13	d
14	e
15	--
16	--
17	--
18	--
19	--
20	--
21	--
22	--
23	L122
24	k

THIRD DOMAIN
OPERATION STACK
630

1	h
2	--
3	--
4	--
5	--
6	--
7	--
8	--
9	i
10	s

FIG. 6



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1	r
1	c
1	h
2	n
2	f
3	b
3	L116
4	a
5	L114
6	o
9	i
10	s
13	d
14	L130
14	e
15	q
22	L136
23	L122
24	k

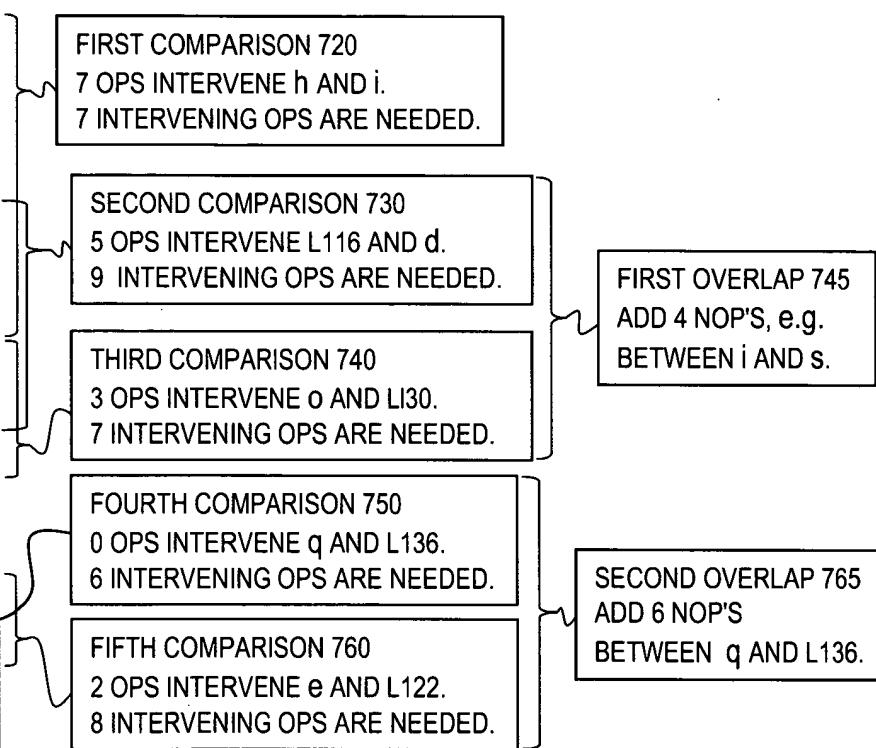


FIG. 7



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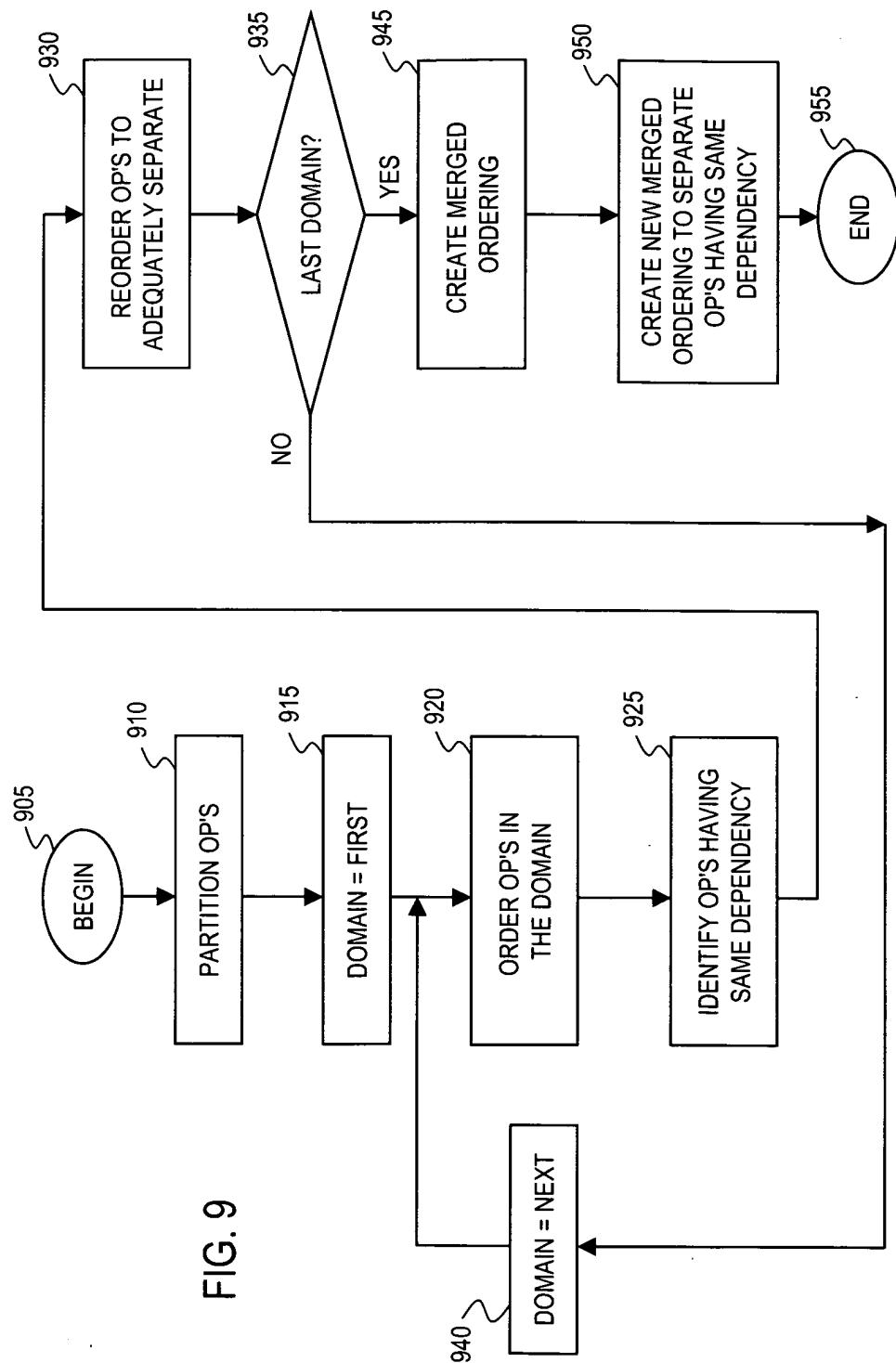
MERGED OPERATION
STACK 810

1	r
2	c
3	h
4	n
5	f
6	b
7	L116
8	a
9	L114
10	o
11	i
12	--
13	--
14	--
15	--
16	s
17	d
18	L130
19	e
20	q
21	--
22	--
23	--
24	--
25	--
26	--
27	L136
28	L122
29	k

FIG. 8



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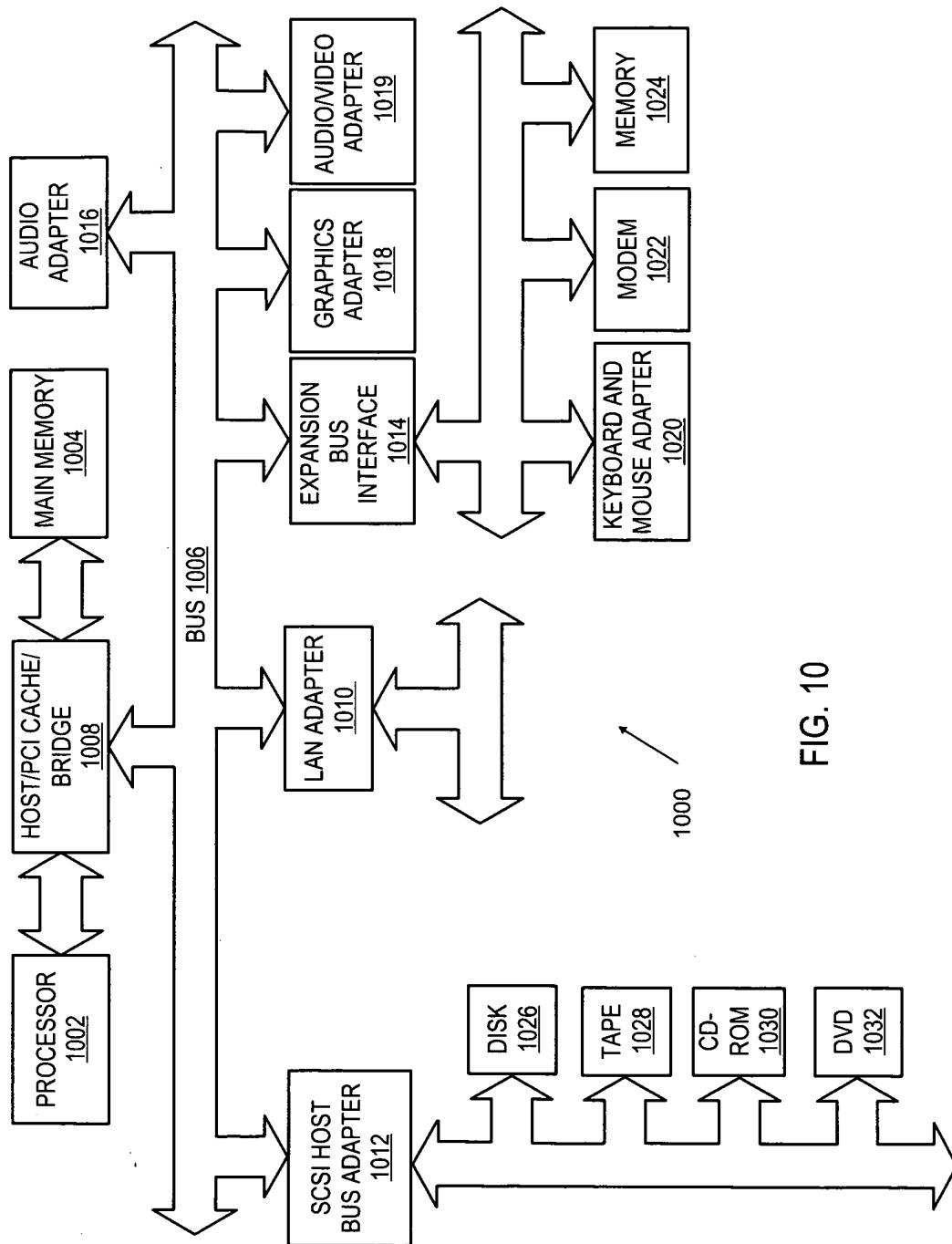


FIG. 10

